



Analysis And Design Of Analog Integrated Circuits, 5Th Ed, Isv

Meyer, Hurst, Gray, Lewis

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The fifth edition retains its completeness, updates the coverage of bipolar technologies, and enhances the discussion of bicmos. It provides a more unified treatment of digital and analog circuit design while strengthening the coverage of cmos. The chapter on non-linear analog circuits has been removed and chapter 11 has been updated to include an operational amplifier example. Models for integrated-circuit active devices bipolar, mos, and bicmos integrated-circuit technology single-transistor and multiple-transistor amplifiers current mirrors, active loads, and references output stages operational amplifiers with single-ended outputs frequency response of integrated circuits feedback frequency response and stability of feedback amplifiers nonlinear analog circuits noise in integrated circuits fully differential operational amplifiers

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